

## AMENDMENTS

### In the Claims

The following is a marked-up version of the claims with the language that is underlined ("\_\_\_\_") being added and the language that contains strikethrough ("—") being deleted:

1. (Currently Amended) A system for providing an internal Universal Serial Bus (USB) port within a computer chassis, the computer chassis internally mounting a motherboard having a first USB header, extending outwardly from the motherboard and configured for mating with a connector of a communication cable, for communicating with an external USB port, said system comprising:

a printed wire board (PWB) supporting a second USB header, a third USB header, a USB hub and the internal USB port, the PWB being mountable at a location within the computer chassis;

the second USB header operative to communicate with the first USB header;

the third USB header operative to communicate with the external USB port;

the USB hub operative to communicate information to and from the first USB header of the motherboard via the second USB header, and to communicate information to and from the external USB port via the third USB header;

the internal USB port being operative to communicate information to and from the motherboard via the USB hub; and

a voltage regulator supported by the PWB, the voltage regulator being operative to receive a first voltage output from the motherboard and to provide, in response thereto, a second, lower voltage output to the USB hub;

wherein each of said second header and said third header extends outwardly from the PWB and is configured to removably mate with a connector of a communication cable.

2. (Original) The system of claim 1, wherein the location at which the PWB is mounted is a location other than a Peripheral Component Interface (PCI) expansion slot of the computer chassis.

3. (Canceled)

4. (Previously Presented) The system of claim 1, wherein the first voltage output is approximately 5 volts, and the second voltage output is approximately 3.3 volts.

5. (Previously Presented) The system of claim 1, wherein the PWB is operative to receive a third voltage output from the motherboard, the third voltage output being routed by the PWB to power the external USB port.

6. (Currently Amended) A system for providing an internal Universal Serial Bus (USB) port within a computer chassis, the computer chassis internally mounting a motherboard having a first USB header, extending outwardly from the motherboard and configured for mating with a connector of a communication cable, for communicating with an external USB port, said system comprising:

a printed wire board (PWB) supporting a second USB header, a third USB header, a USB hub and an internal USB port, the PWB being mountable at a location within the computer chassis;

the second USB header operative to communicate with the first USB header;

the third USB header operative to communicate with the external USB port;

the USB hub operative to communicate information to and from the first USB header of the motherboard via the second USB header, and to communicate information to and from the external USB port via the third USB header; and

the internal USB port being operative to communicate information to and from the motherboard via the USB hub;

wherein:

the chassis has mounts extending into the interior thereof; and

the PWB has apertures formed therethrough, each of the apertures being operative to receive one of the mounts such that insertion of the mounts into the apertures secures the PWB to the chassis;

wherein each of said second header and said third header extends outwardly from the PWB and is configured to removably mate with a connector of a communication cable.

7. (Original) The system of claim 6, wherein the mounts form interference fits with the apertures when the mounts inserted within the apertures.

8. (Currently Amended) The system of claim 1, further comprising:

a first USB cable having a first connector, operative to mate and interconnect with the first USB header of the motherboard, and a second connector operative to mate and interconnect with the second USB header; and

a second USB cable having a third connector, operative to mate and interconnect with the third USB header such that the third USB header communicates with the external USB port.

9. (Previously presented) A computer system comprising:

a chassis defining an interior;

a first Universal Serial Bus (USB) port externally mounted to the chassis;

a motherboard mounted within the interior of the chassis, the motherboard having a first USB header for communicating with the first USB port; and

a daughter card mounted within the interior of the chassis, the daughter card communicating with the motherboard and having a second USB port, a USB hub, a second USB header, and a third USB header;

the USB hub being operative to communicate information to and from the first USB header of the motherboard via the second USB header of the daughter card, and to communicate information to and from the first USB port via the third USB header of the daughter card;

an internal USB port being operative to communicate information to and from the motherboard via the USB hub and the second USB header of the daughter card; and

a voltage regulator supported by the daughter card, the voltage regulator being operative to receive a first voltage output from the motherboard and to provide, in response thereto, a second, lower voltage output to the USB hub;

wherein each said header is configured to removably mate with a connector of a corresponding communication cable.

10. (Original) The system of claim 9, wherein:

the chassis has a Peripheral Component Interface (PCI) expansion slot; and

the daughter card is mounted at a location other than the PCI expansion slot.

11. (Original) The system of claim 9, wherein the motherboard controls continuity of power to the daughter card.
12. (Canceled)
13. (Previously Presented) The system of claim 9, wherein the first voltage output is approximately 5 volts, and the second voltage output is approximately 3.3 volts.
14. (Original) The system of claim 9, wherein the daughter card is operative to receive a third voltage output from the motherboard, the third voltage output being routed by the daughter card to power the first USB port.
15. (Original) The system of claim 9, further comprising:  
means for securing the daughter card to the chassis.
16. (Currently Amended) A computer system comprising:  
a chassis defining an interior;  
a first Universal Serial Bus (USB) port externally mounted to the chassis;  
a motherboard mounted within the interior of the chassis, the motherboard having a first USB header for communicating with the first USB port; and  
a daughter card mounted within the interior of the chassis, the daughter card communicating with the motherboard and having a second USB port, a USB hub, a second USB header, and a third USB header;

the USB hub being operative to communicate information to and from the first USB header of the motherboard via the second USB header of the daughter card, and to communicate information to and from the first USB port via the third USB header of the daughter card; and

the internal USB port being operative to communicate information to and from the motherboard via the USB hub and the second USB header of the daughter card;

wherein:

the system further comprises a mount extending into the interior of the chassis; and  
the daughter card has an aperture for receiving the mount such that insertion of the mount into the aperture secures the daughter card to the chassis; and

wherein each said header is configured to removably mate with a connector of a corresponding communication cable.

17. (Original) The system of claim 16, wherein the mount forms an interference fit with the aperture when inserted therein.

18. (Original) The system of claim 9, further comprising:

a first USB cable operative to interconnect the first USB header of the motherboard with the second USB header of the daughter card; and

a second USB cable operative to interconnect the third USB header of the daughter card with the first USB port.

19. – 23. (Canceled)